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CLAIMS

1	1. A logically partitioned data processing system,
2	comprising:
3	a plurality of logical partitions;
4	a plurality of operating systems, each assigned to
5	one of said plurality of logical partitions;
6	a plurality of memory locations, each location
7	assigned to one of said plurality of logical partitions;
8	a data transmission bus;
9	at least one terminal bridge connected to said data
10	transmission bus;
_1 1	a plurality of input/output adapters, each
2 3	associated with a different one of said plurality of
13	logical partitions, said input/output adapters being
14	connected to said terminal bridge; and
14 15	means for preventing transmission of data between a
16	given one of said input/output adapters which is
<u>"1</u> 7	associated with a first one of the plurality of logical
18	partitions, and memory locations unassigned to said first
9	one of said plurality of logical partitions.
= 1	2. The logically partitioned data processing system
2	of Claim 1 wherein said data transmission bus is a PCI
3	bus, and further comprising:
4	a PCI host bridge connected to said PCI bus; and
5	an input/output bus connected to said PCI host
6	bridge.
1	3. The logically partitioned data processing system
2	of Claim 1 wherein said terminal bridge has a plurality
3	of sets of range registers, each associated with a

respective one of said input/output adapters.

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- 4. The logically partitioned data processing system of Claim 3 further comprising an arbiter which selects one of said input/output adapters to use said data transmission bus, wherein said transmission preventing means assigns one of said sets of range registers based on a grant signal from said arbiter.
- 5. The logically partitioned data processing system of Claim 3 wherein said sets of range registers contain direct memory access addresses which limit operations that may be placed onto said data transmission bus by said input/output adapters.
- 6. The logically partitioned data processing system of Claim 3 wherein said sets of range registers are programmable.

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7. A method of preventing an operating system image within a logically partitioned data processing system from fetching or corrupting data from a memory location allocated to another operating system image within the data processing system, the method comprising the steps of:

receiving a request from the operating system image to access a given one of a plurality of input/output adapters each associated with a different one of a plurality of logical partitions of the data processing system, wherein the input/output adapters are connected to a single terminal bridge; and

accessing the given input/output adapter using memory mapped to the operating system image.

8. The method of Claim 7 wherein said accessing step includes the steps of:

transmitting the request to a PCI host bridge using an input/output bus; and

conveying the request from the PCI host bridge to the terminal bridge using a PCI bus.

- 9. The method of Claim 7 wherein said accessing step utilizes one of a plurality of sets of range registers of the terminal bridge, each associated with a respective one of the input/output adapters.
- 10. The method of Claim 9 wherein said accessing step further utilizes an arbiter which selects one of the input/output adapters, to assign one of the sets of range registers based on a grant signal from the arbiter.
- 11. The method of Claim 9 further comprising the step of associating each of the sets of range registers

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3	with direct memory access addresses which limit access by
1	the input/output adapters.

12. The method of Claim 9 further comprising the step of programmably loading the sets of range registers.

 13. A computer program product for use in a data processing system for preventing an operating system image within a logically partitioned data processing system from fetching or corrupting data from a memory location allocated to another operating system image within the data processing system, the computer program product comprising:

a storage medium; and

program instructions stored on said storage medium for receiving a request from the operating system image to access a given one of a plurality of input/output adapters each associated with a different one of a plurality of logical partitions of the data processing system, wherein the input/output adapters are connected to a single terminal bridge, and for accessing the given input/output adapter using memory mapped to the operating system image.

- 14. The computer program product of Claim 13 wherein the request comprises an input/output adapter identity, a memory address range to be mapped, and a direct memory access range, and said program instructions further determine that the identity of the input/output adapter, the memory address range, and the direct memory access range are allocated to the operating system image.
- 15. The computer program product of Claim 13 wherein said program instructions access the input/output adapter utilizing one of a plurality of sets of range registers of the terminal bridge, each associated with a respective one of the input/output adapters.

1 16. The computer program product of Claim 15 wherein 2 said program instructions further load the sets of range 3 registers.